

# MANAGING METHOD FOR MEMORY OF IC CARD

**Publication number:** JP62049595

**Publication date:** 1987-03-04

**Inventor:** HARADA MASAYUKI;  
YAMAGUCHI TETSUO; KON  
TOSHINORI; INOUE SHINJI;  
FUJITA MIKIO

**Applicant:** MATSUSHITA ELECTRIC IND CO  
LTD

**Classification:**

- **International:** G06K19/073; B42D15/02;  
B42D15/10; G06F12/14;  
G06F21/24; G06K19/00;  
G06K19/073; B42D15/02;  
B42D15/10; G06F12/14;  
G06F21/00; G06K19/00; (IPC1-7):  
B42D15/02; G06F12/14;  
G06K19/00

- **European:**

**Application number:** JP19850190821 19850829

**Priority number(s):** JP19850190821 19850829

[Report a data error here](#)

## Abstract of JP62049595

PURPOSE: To prevent the leakage of a secret between enterprises even if the respective data are made to coexist in one sheet of card, by making each different pass-word correspond to plural memory areas, respectively, which are divided into blocks. CONSTITUTION: The contents of a memory 8 of an IC card 6 are read by an external control part 10 which is coupled to an external terminal 9 connected to a microprocessor 7. A data area of this memory 8 is divided into plural blocks, each different pass-word is allocated to every block in accordance with the respective enterprises, and it is controlled so that read of the corresponding data cannot be executed unless a collation by the pass-word is executed. Accordingly, even if the respective data are made to coexist in one sheet of card, the leakage of a secret between the enterprises can be prevented.

---

Data supplied from the **esp@cenet** database - Worldwide

